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Statistics **Q. 3.15:**

Simplify the following

Boolean function F,

together with the

don't-care conditions

d, and Q. 4.27: A

combinational circuit is

specified by the

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following three Boolean functions Q. 3.6:

~~Simplify the following Boolean expressions, using four variable maps: (a) $A'B'C'D' + AC'D' +$~~

~~Q. 2.16: The logical sum of all minterms of a Boolean function of n variables is 1. (a) Prove the~~

Implementation of boolean function using multiplexer in simple

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*Q. 2.19: Express
following function as
sum of minterms and
product of maxterms:
 $F = B'D + A'D + BD$ Q.*

*3.12: Simplify the
following Boolean*

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functions to product-of-sums form: (a)

$F(w,x,y,z)=sum(0,1,2,$

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*Implement the following
Boolean function with a
multiplexer (HDL—see
Problem 4.48) Q. 5.4: A*

~~*PN flip-flop has four*~~

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~~operations: clear to 0, no
change, complement,
and set to 1, when~~

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Q. 6.25: It is necessary
to generate six repeated
timing signals T0
through T5 similar to
the ones4-I-Type

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*4.10: Design a four-bit
combinational circuit*

*2's complementer. (The
output generates the 2's*

Q. 3.9: Find all the
prime implicants for the
following Boolean
functions, and determine

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7- Shift Add Multiplier
Version 2 | Shift Add
Multiplication
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*Chapter 5: Design
Procedure (Sec. 5.8)*

Q. 6.24: Design a
counter with T
flip?flops that goes
through the following
binary repeated
sequence

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~~Implement the Boolean
function $F = xy + x'y' +$
 $y'z$ (a) With AND, OR,
and inverter gates~~

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