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forbidden latency, collision vector, state diagram

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Bibliographic information. Title: Advanced Computer Architecture: Author: Hwang: Publisher: McGraw-Hill Education (India) Pvt Limited, 2003:

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Problems: 251

This text presents the latest technologies for parallel processing and high performance computing. It deals with advanced computer architecture and parallel processing systems and techniques, providing an integrated study of computer hardware and software systems, and the material is suitable for use on courses found in computer science, computer engineering, or electrical engineering

#### Computer Architecture/Software Engineering

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disaster recovery Designed for undergraduate or graduate students taking a distributed systems course—each chapter includes exercises and further reading, with lecture slides and more available online

Computer architecture deals with the physical configuration, logical structure, formats, protocols, and operational sequences for processing data, controlling the configuration, and controlling the operations over a computer. It also encompasses word lengths, instruction codes, and the interrelationships among the main parts of a computer or group of computers. This two-volume set offers a comprehensive coverage of the field of computer organization and architecture.

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